

1000
 900
 800
 700
 600
 500
 400
 300
 200
 100
 0

Abstract of Disclosure

The present invention provides a data recovery circuit for generating an output signal that is synchronized with an input signal. The data recovery circuit includes a charge pump, a first filter, an oscillator, a switch circuit, and a second filter. When the charge pump operates, the switch circuit will disconnect the first filter from the oscillator. Additionally, when the charge pump stops operating, the switch circuit will connect the first filter and the oscillator such that the oscillator adjusts a frequency or phase of the output signal according to the output voltage of the first filter.

Figures

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